



# 1200V Silicon Carbide Schottky Diode

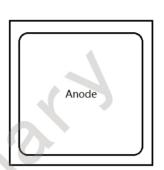






### **Features**

- Negligible reverse recovery
- High surge current
- Positive temperature coefficient
- Higher frequency
- Halogen-free / RoHS compliant



# **Applications**

- SMPS
- Solar inverter
- Data Center
- UPS

# Benefits

- High-speed switching
- Low heat dissipation requirements
- Reduce size and cost of the system
- High-reliability

#### **Die Information**

Wafer Size	150 mm				
Die Size	3200 × 3200 um <sup>2</sup> (exclude SL)				
Scribe Line Size	80 um				
Die Thickness	175 um				
Anode Pad Opening Size	2870 × 2870 um <sup>2</sup>				
Gross Die	1452 ea				
Top Metallization	Al, 4um				
Back Metallization	Ti/Ni/Ag, 2.5um				
Frontside Passivation	Polyimide				
Wire Bond	AI, 15mil×2 (recommend)				

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# GSD20120

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# **Electrical Characteristics (Wafer Type)**

### Maximum Ratings\*2 (Tc=25°C unless otherwise noted)

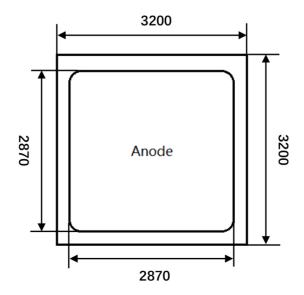
Symbol	Parameter	Value	Unit	
V <sub>RRM</sub>	Repetitisve peak reverse voltage	1200	٧	
I <sub>F</sub>	Continuous forward current	Tc=159℃	20	Α
I <sub>FSM</sub>	Non-repetitive forward surge current	t <sub>p</sub> =10ms,Half sine pulse	120	Α
I <sub>FRM</sub>	Repetitive Peak Forward Surge Current	$t_p$ =10ms,Half sine pulse	105	Α

<sup>\*2.</sup> Based on TO-220-2 package

### Static Electrical Characteristics (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Value			l leit
			Min.	Тур.	Max.	Unit
$V_R$	Reverse blocking voltage	$I_R$ =250uA, Tc=25°C	1200	-	-	V
I <sub>R</sub>	Reverse current	V <sub>R</sub> =1200V, Tc=25℃	-	2.0	150	uA
V <sub>F</sub> Forward voltage	I <sub>F</sub> =10A, Tc=25℃	-	1.25	-	V	
	roiward voitage	I <sub>F</sub> =20A, Tc=25℃	-	1.44	1.70	V

## Die Layout (Unit : um)



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