



650V Silicon Carbide Schottky Diode

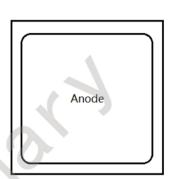






Features

- Negligible reverse recovery
- High-speed switching
- Positive Temperature Coefficient
- Temperature-Independent Switching
- Halogen-free / RoHS compliant



Applications

- Switch mode power supply
- Solar inverter
- Data Center
- Uninterruptible power supply

Benefits

- Higher frequency
- Low heat dissipation requirements
- Reduce size and cost of the system
- High-reliability

Die Information

Wafer Size	150 mm
Die Size	$1600 \times 1600 \text{ um}^2$ (exclude SL)
Scribe Line Size	80 um
Die Thickness	175 um
Anode Pad Opening Size;	1400 × 1400 um²
Gross Die	5596 ea
Top Metallization	Al, 4um
Back Metallization	Ti/Ni/Ag, 2.5um
Frontside Passivation	Polyimide
Wire Bond	Al, 15mil×1 (recommend)

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GSD08060

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Electrical Characteristics (Wafer Type)

Maximum Ratings² (Tc=25°C unless otherwise noted)

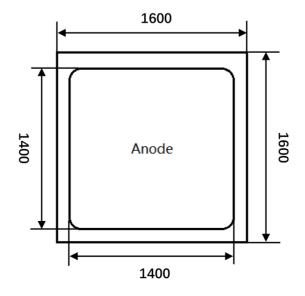
Symbol	Parameter	Value	Unit	
V _{RRM}	Repetitisve peak reverse voltage	650	V	
I _F	Continuous forward current	Tc=154℃	8	А
I _{FSM}	Non-repetitive forward surge current	t_p =10ms,Half sine pulse	76	А
I _{FRM}	Repetitive Peak Forward Surge Current	t_p =10ms,Half sine pulse	67	А

^{*2} Based on TO-220-2 package

Static Electrical Characteristics (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Value			l loit
			Min.	Тур.	Max.	Unit
V_R	Reverse blocking voltage	I _R =50uA	650	-	-	\
I_R	Reverse current	V _R =650V	-	7	60	uA
V _F Forward voltage	I _F =4A	-	1.17	-	V	
	roiward voitage	I _F =8A	-	1.39	1.6	V

Die Layout (Unit : um)



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Announcement

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